FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

Title of Invention	A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA

Application Number :

Date :

First Named Applicant: JOHN M. COHN

Attorney Docket Number: BUR920040001US1

TOTAL FEE AUTHORIZED $ 1076

Patent fees are subject to annual revisions on or about October 1st of each year.

Filing as large entity

BASIC FILING FEE

Fee Description	Fee Code	Amount $	Fee Paid $
Utility Filing Fee	1001	770	770
		Subtotal For Basic Filing Fees: $ 770	

EXTRA CLAIM FEES

Fee Description	Extra Claim	Fee Code	Amount $	Fee Paid $
Total Claims : 30	10	1202	18	180
Independent Claims : 4	1	1201	86	86
			Subtotal For Extra Claims Fees: $ 266	

ASSIGNMENT FEES

Fee Description	Property Number	Quantity	Fee Code	Amount $	Fee Paid $
Recording Each Patent Assignment Per Property Fee	00000000	1	8021	40	40
				Subtotal For Additional Fees: $40	

AUTHORIZED BILLING INFORMATION

The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit account number: 090456

Access Code ****

Deposit name: International Business Machines Corporation

Deposit authorized name: Michael J. LeStrange

Signature: /Michael J. LESTRANGE, Reg. #53,207/

Date (YYYYMMDD):	2004-05-25

Charge Assignment Fees Required Under 37 C.F.R. Section 1.21 (h).

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.